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Date: <u>September 20, 2001</u>

Docket No.: 0033-0768P

Assistant Commissioner for Patents
Box PATENT APPLICATION
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s):

KOZONO, Yasufumi

TANAKA, Yoshimichi; NOIRI, Hiroshi

IMAMATSU, Takeshi

For:

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DESIGN SUPPORT SYSTEM CAPABLE OF REDUCING DESIGN ERROR

Enclosed are:

<u>X</u>	A	specification	consisting	ΟÏ	<u>16</u>	pages
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X 14 sheet(s) of FORMAL drawings

 \sqrt{X} An assignment of the invention - \$40.00 Recording Fee

X Certified copy of Priority Document(s)

X Executed Declaration X Original Photocopy

____ Applicant claims small entity status in accordance with 37 CFR 1.27

___ Application Data Sheet in accordance with 37 C.F.R. 1.76

	0033-0768P							
Prelimi	nary Amendment							
Informa	tion Disclosure	e Statemer	nt, PTO-	1449 and	d re	eference(s	;)	
Other _								
Applica	nt requests ea:	rly public	cation					
The filing f	ee has been cal	lculated a	as shown	below:				
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FOR	NO. FILED NO	O. EXTRA	RATE	FEE		RATE	FEE	
BASIC FEE	*********** *:		**** **** ****	\$710.00	or	* * * * * * * * * * * *	\$355.00	
TOTAL	9 - 20 =	0	x18 =\$	0.00	or	x 9 = \$	0.00	
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Falls Church, VA 22040-0747 Telephone: (703) 205-8000 If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

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